

Customized COTS: An Architecture Overview

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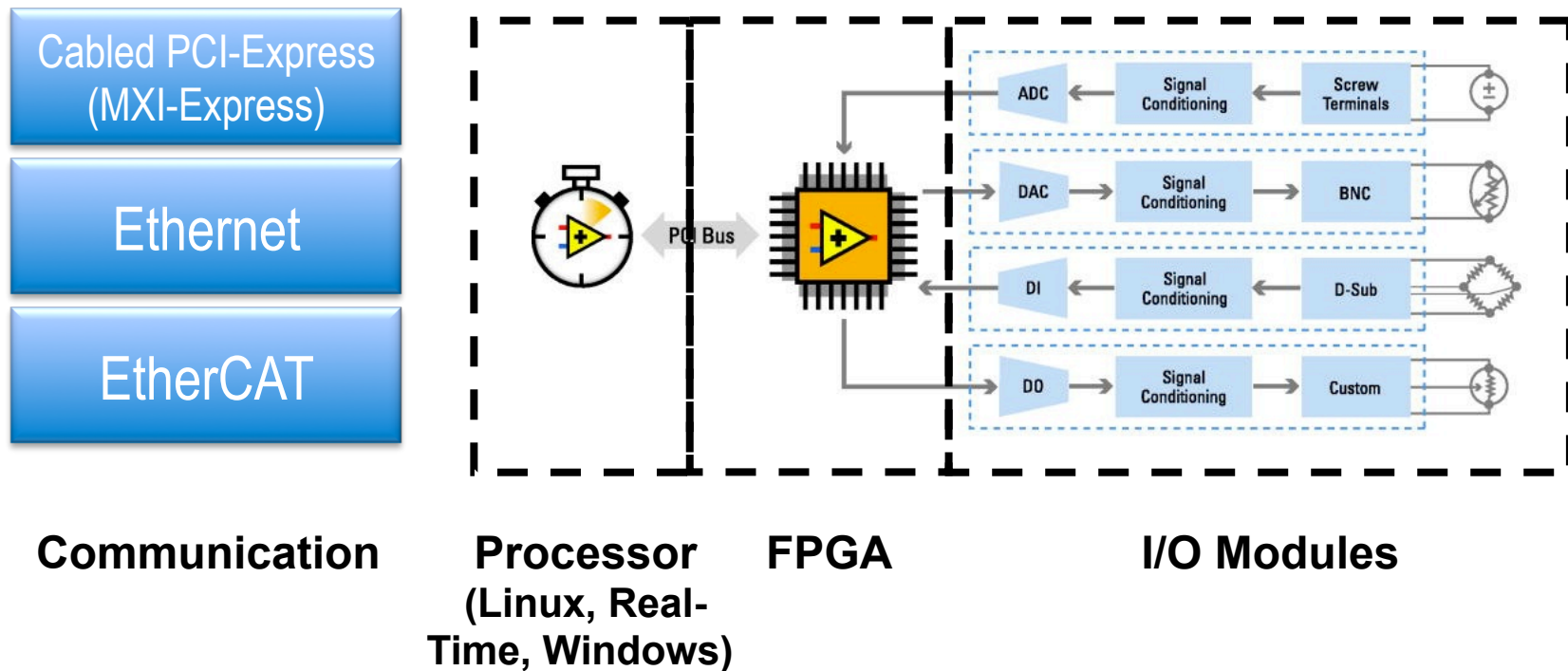
National Instruments

Agenda

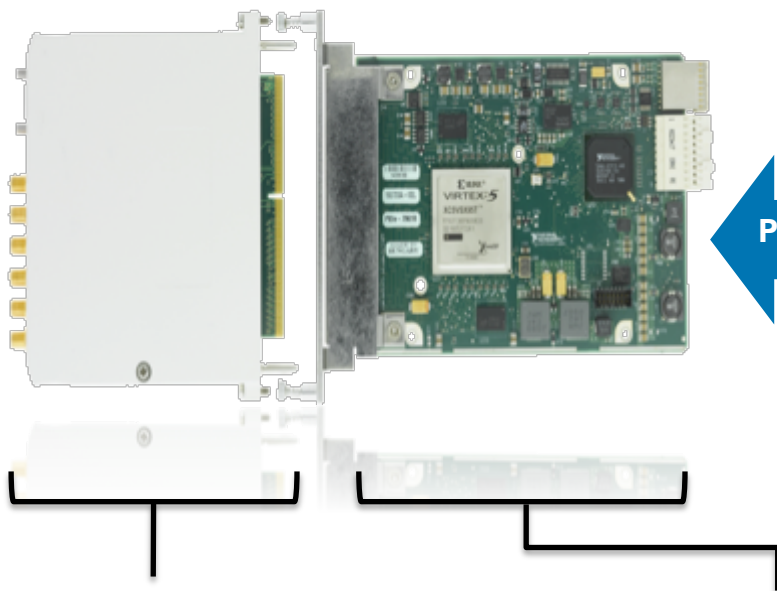
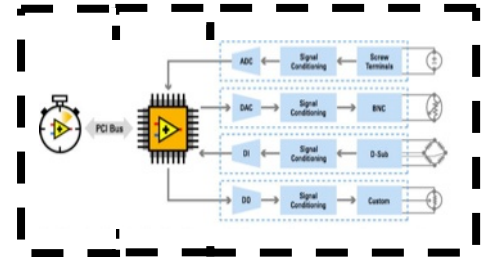
- Customized COTS (commercial of the shelf) Technology
 - Architecture
 - Integrating IP
 - Linux
- Typical COTS-Based System
 - Advantages
- Life cycle management and services

Combining COTS With Your Design

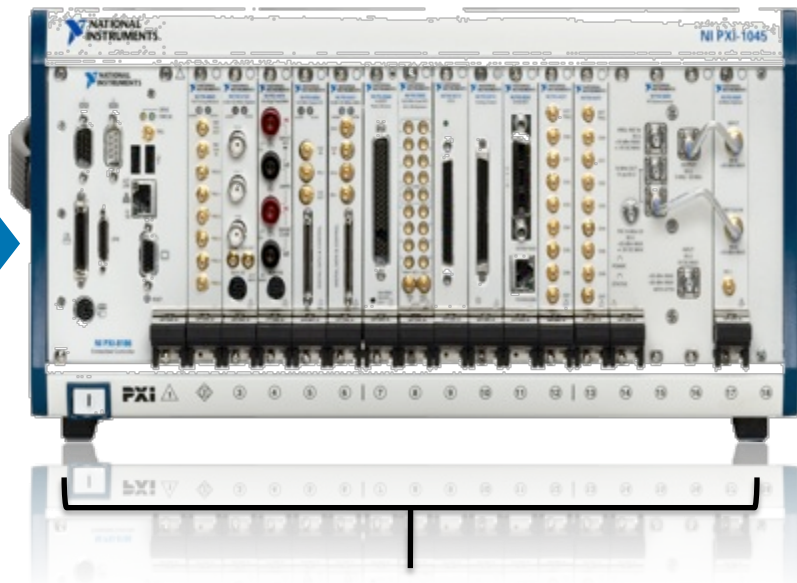
RIO Architecture



NI FlexRIO



PXI/ PXIe



NI FlexRIO Adapter Module

- Interchangeable I/O
- Digital or analog
- NI FlexRIO Adapter Module Development Kit (MDK)

NI FlexRIO FPGA Module

- Virtex-5 FPGA
- 132 digital I/O lines
- Up to 512 MB of DRAM
- Peer-to-peer data streaming

PXI Platform

- Data transfer
- Synchronization
- Clocking/triggers
- Power/cooling

NI FlexRIO Partner Modules and MDK



100 MHz
PPMU

Camera Link
and GigE

Multi-gigabit
optical

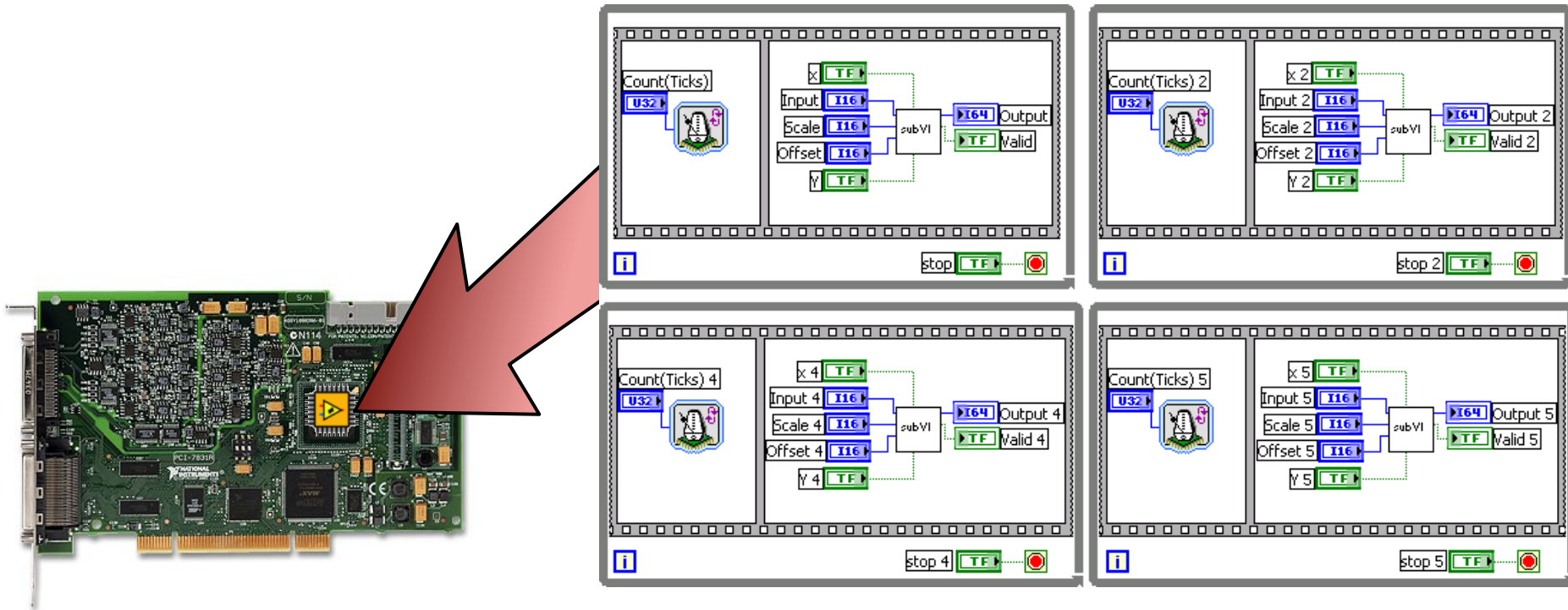
Dual gigabit
Ethernet

Video and
Automotive

Time to Digital
Convertor

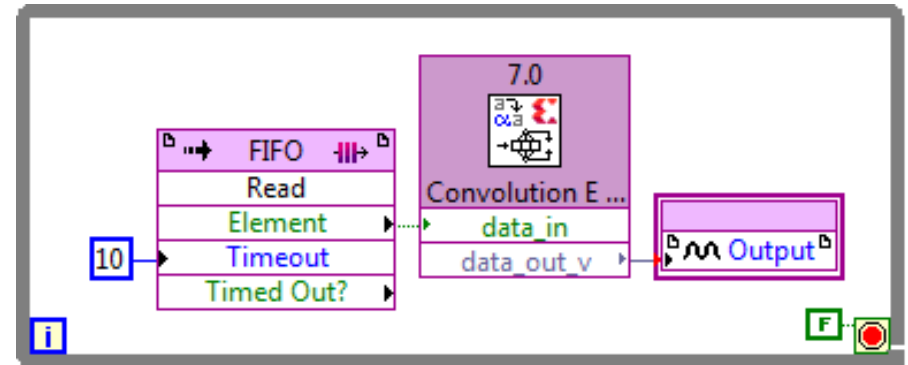
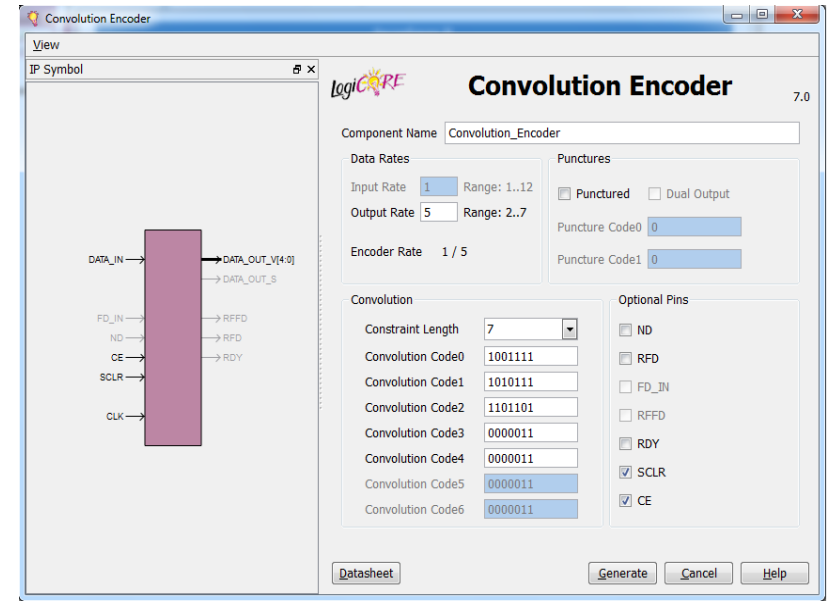
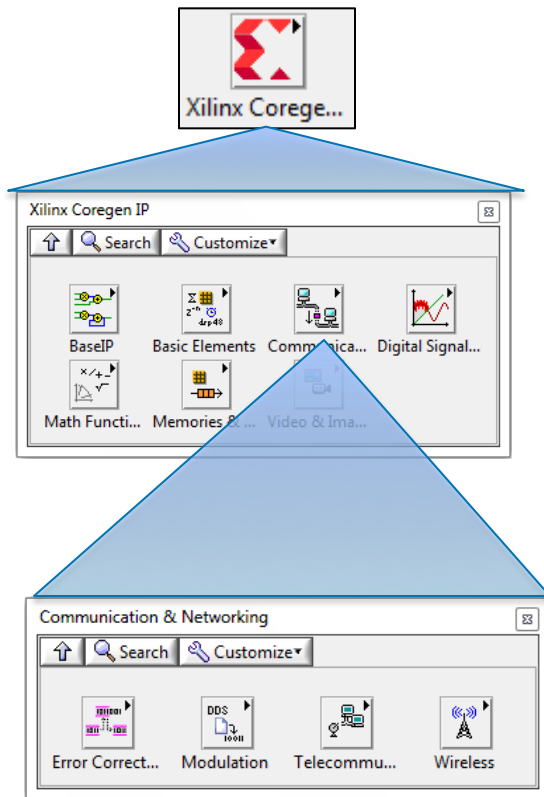
Module
Development Kit

FPGA Programming: Multicore, Multiprocessor Development

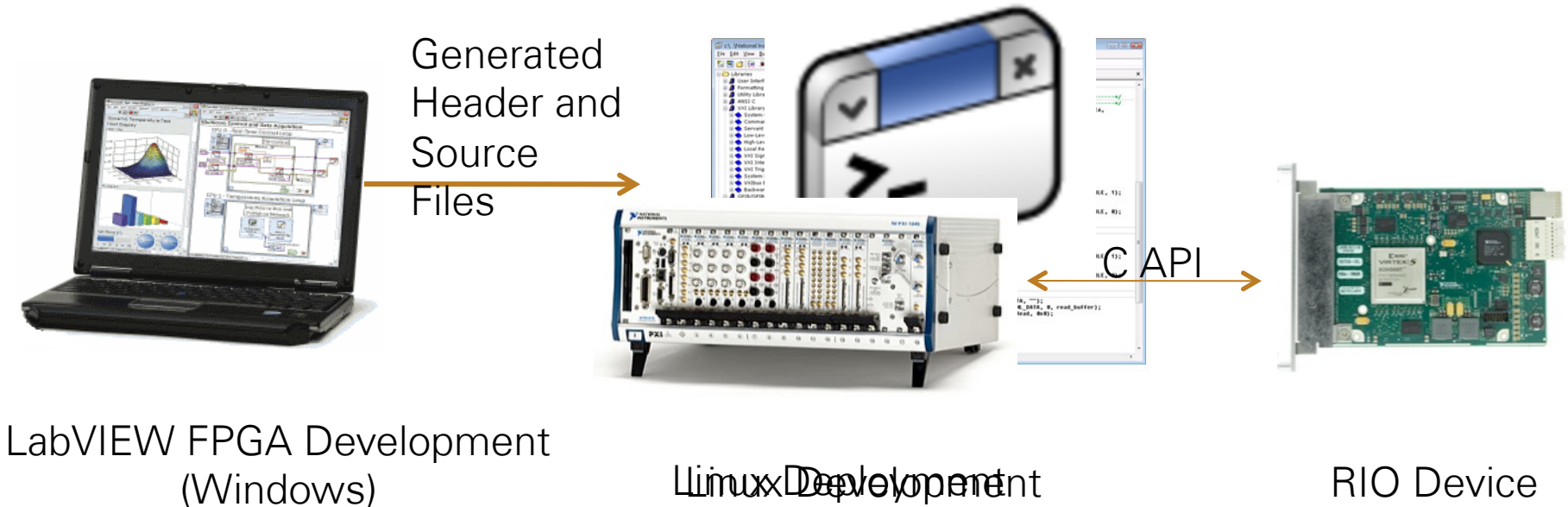


LabVIEW FPGA

Direct Access to Preexisting Xilinx CORE Generator IP Libraries



Linux: C Interface for FPGA

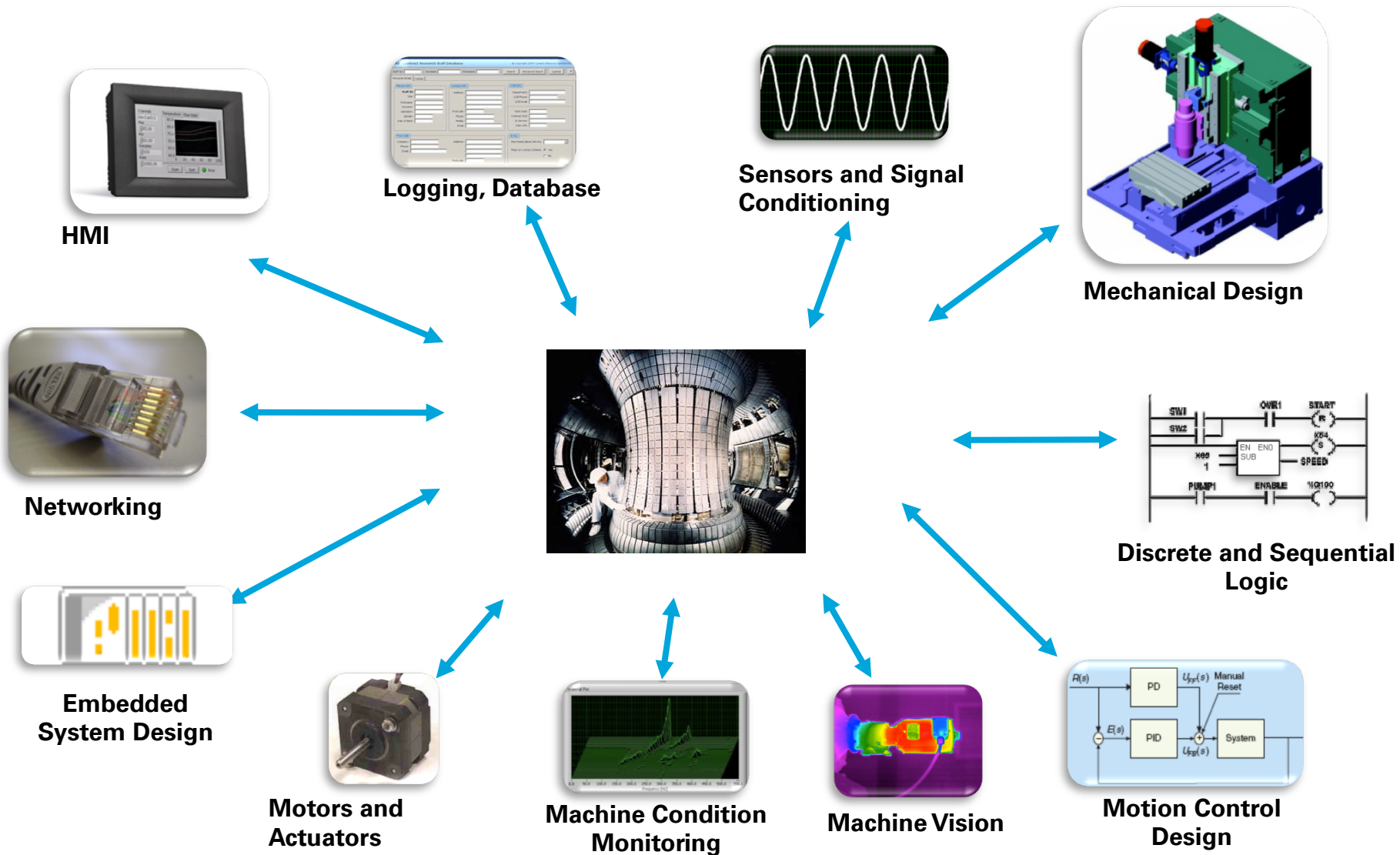


1. Develop LabVIEW FPGA VI, compile bitfile, and generate C API.
2. Develop and build C/C++ application with generated C API.
3. Deploy built application and bitfile to Linux target, and run.

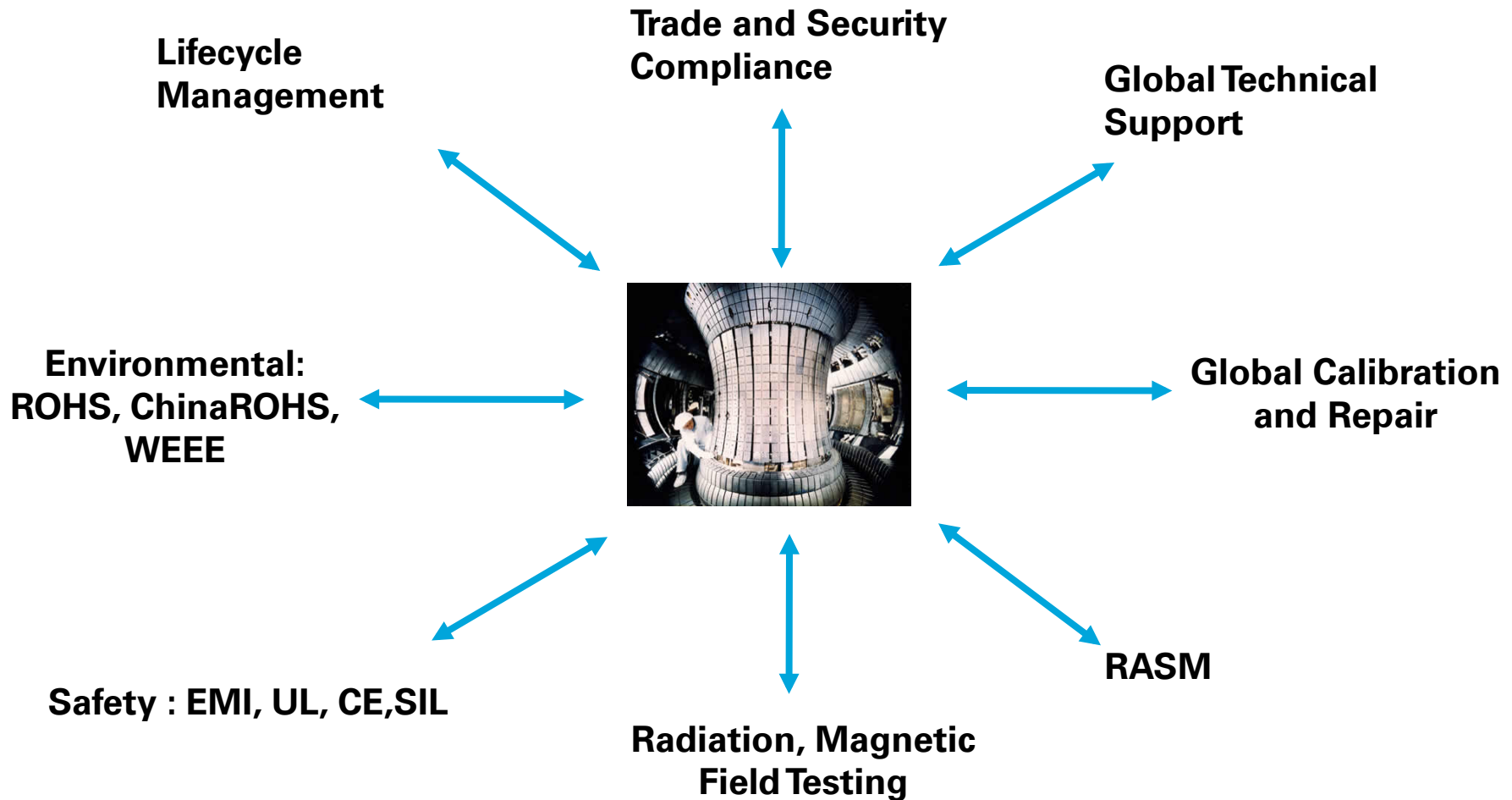
Advantages of Customized COTS Architecture

- Ability to incorporate multiple functionality in one system
- Integration with multiple timing systems
 - Event receiver/generators, White Rabbit, IEEE 1588
- Access to raw data through high performance PXIe bus
- Integration to control systems
 - EPICS, TANGO and other middleware

System Complexity : Technological



System Complexity : Logistical

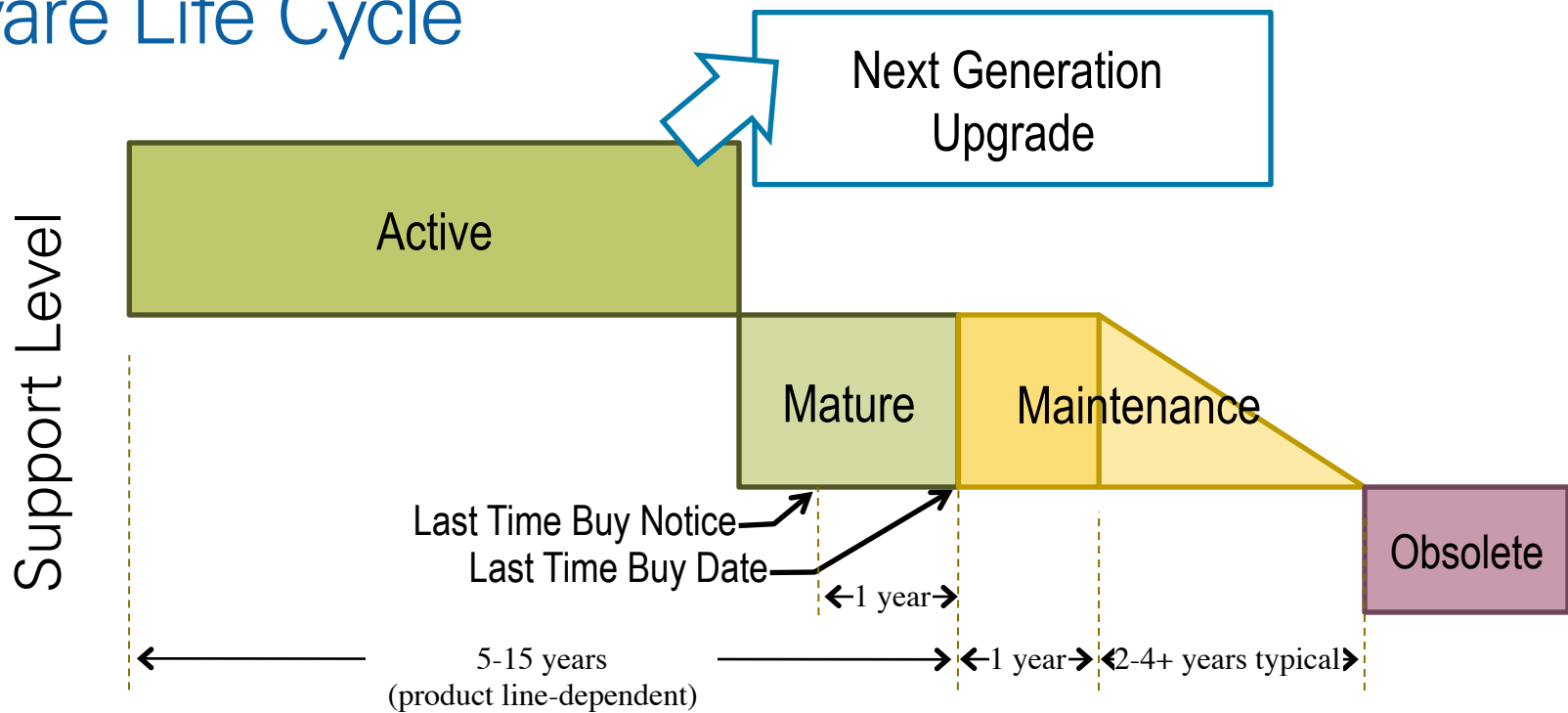


Reliability Lab Testing

- 24/7 testing of multiple systems for years
- Temperature variation, vibration, 'dirty power' testing

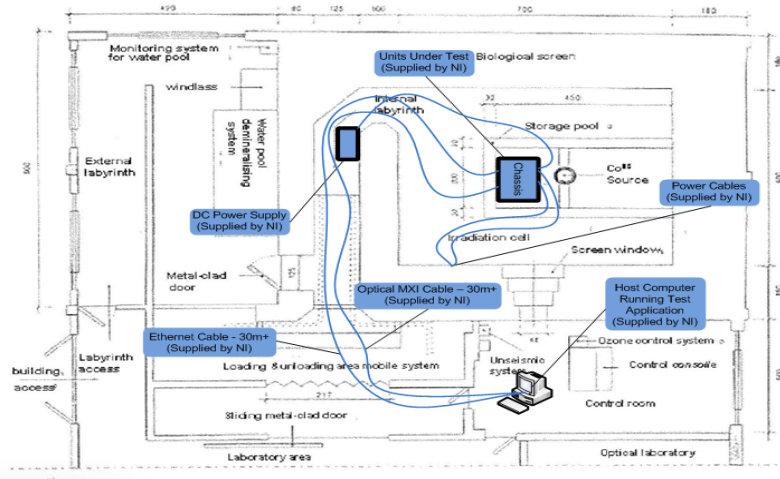


Hardware Life Cycle



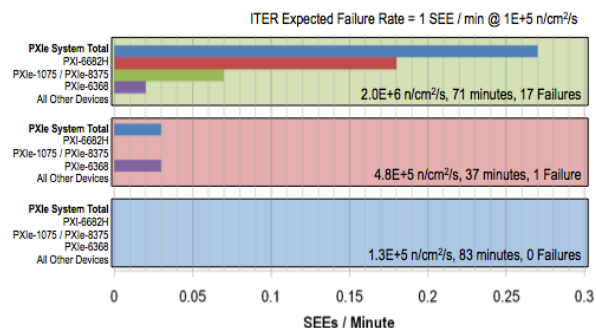
	Active	Mature	Maintenance		Obsolet e
Purchase new	Yes	Yes	No	No	No
Repair	Yes	Yes	Yes	Reasonable effort	No
Calibration	Yes	Yes	Yes	Reasonable effort	No
Service Agreements	Yes	Yes	Yes	Yes	Yes

Gamma and Neutron Testing



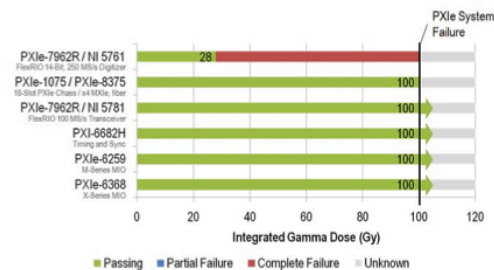
Results Published

PXIe SEEs per min – Neutron Test Summary



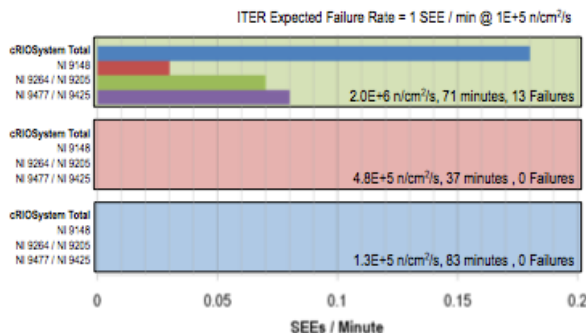
cRIO and PXIe systems ran at $1.3E+5$ n/cm²/s for 83 minutes with no errors

PXIe System Gamma Irradiation Test Summary

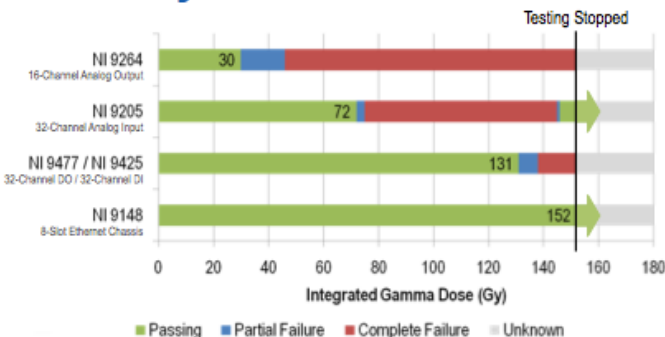


- Most (1 PXI / 1 cRIO) failed devices exceeded expected failure dose of 50 Gy
- More than half of the devices exceeded the maximum expected failure dose of 100 Gy

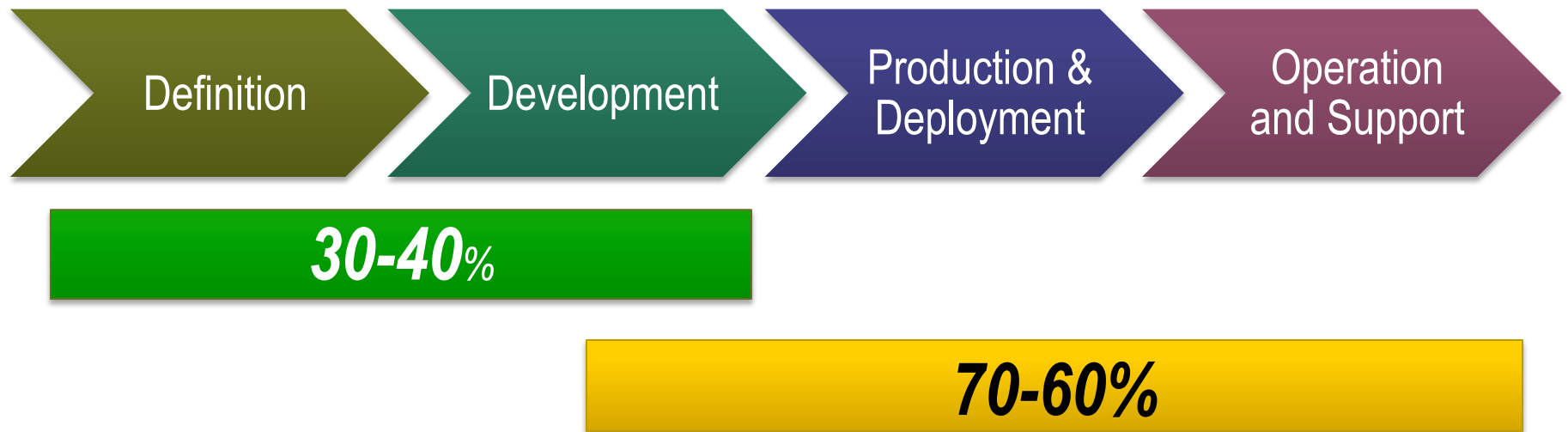
cRIO SEEs per Minute



cRIO System Irradiation Test Summary



Cradle to Grave Investment



Summary

- Varying application requirements
- FPGA-based architecture allows for user customization
 - FlexRIO provides open architecture
 - User programmable FPGA's with LabVIEW FPGA
- Scalability
 - Integration with control and timing systems
- Life cycle management and services
 - Reliability and availability lab
 - Special radiation and magnetic testing